

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of **09/789,771**:

In re the Application of: **IIDA, Kenji**

Group Art Unit: **1763 (expected)**

Serial No.: **(NEW)**

Examiner: **Sylvia R. MACARTHUR (expected)**

Filed: **July 7, 2003**

For: **MULTILAYER CIRCUIT BOARD AND METHOD OF MANUFACTURING
THE SAME**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 7, 2003

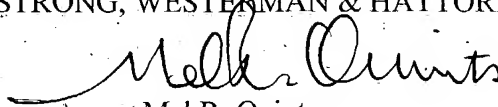
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. **09/789,771**.

The Director is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: **PTO-1449**

INFORMATION DISCLOSURE STATEMENT PTO-1449	Atty. Docket No. 010194A	Serial No. New Divisional Appln.
	Applicant(s): IIDA, Kenji	
	Filing Date: July 7, 2003	Group Art Unit: 1763 (expected)

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	5,916,453	Beilin et al.	06/29/99	216	38	
_____	AB						
_____	AC						
_____	AD						
_____	AE						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AF				
_____	AG				
_____	AH				

OTHER DOCUMENTS

_____	AI	
_____	AJ	
_____	AK	
Examiner		Date Considered